July 20, 2021

JOB OPPORTUNITY: FPGA DESIGN ENGINEER

eVS designs computer vision software and FPGA IPs on embedded architectures. We are a high-tech company strongly oriented towards research and innovative solutions primarily for the automotive industry, but also other industrial targets such as aerospace, medical and machine vision. We provide design and verification services for FPGA IPs and work with a wide range of technologies.

We are seeking for an experienced FPGA Engineer to focus mainly on computer vision and machine/deep learning projects, covering the entire pipeline, from the design and development to simulation and test. As eVS employee, you will join a challenging and stimulating work environment always open to acknowledge your own ideas and contributions for continuous improvement.

Required qualifications:

- Background in Electronic Engineering or Computer Science with a focus on Computer Architectures.
- Familiarity with computer vision and machine/deep learning algorithms and techniques.
- At least three years of experience working with FPGA and/or ASIC designs.
- Familiarity with the FPGA development flow. Knowledge of the Xilinx tool flows is preferrable.
- Expert knowledge of VHDL language. Knowledge of System Verilog language is a plus.
- Experience with Python. Knowledge of MATLAB is a plus.
- Knowledge of C/C++ is desiderable.
- Experience on building bit-accurate software models for algorithms.
- Extensive experience verifying complex designs is desired. Knowledge of UVM is considered a plus.
- Familiarity with the culture of continuous integration and related tools. Expertise with GIT is required.
- Excellent communication skills and ability to work in a highly collaborative environment.
- Fluent English, spoken and written.

Place of work: Verona, Italy. Remote work can be an option to some extent.

The eVS company is an equal opportunity employer.
EMail us at **info@evsys.it** to apply for this position with explicit consent to personal data processing (D.lgs n. 198/2006)