

Europass Curriculum Vitae



Personal information

Surname(s) / First name(s)

Address(es)

Telephone(s)

Email(s)

Nationality(-ies)

Date of birth

Gender

Professional Goals

Demrozi Florenc

Strada Ormedello,4 , 46048, Roverbella (Mantova)

(+39) 0458027048 Mobile: (+39) 3493438100

florenc.demrozi@univr.it

Albanian

27/05/1991

Male



Expand my knowledge in the field of modelling and verification of embedded systems working closely with highly qualified and culturally very rich professionals. Also expand my knowledge about relatively new topics such as Internet of Things (IoT), Ambient Intelligence and Ambient Assisted Living, finding an application of such research areas to the Embedded System Area and *vice versa*.

Education and training

October 2016 – September
2019

Ph.D. student in Computer Science

University of Verona, Verona (IT), XXXII Ciclo (2016 – 2019)

Key research areas: *Design and Verification of Embedded System, Ambient Intelligence, IoT and Ambient Assisted Living (AAL)*

Title of the thesis: *Virtual Coaching for Assisting in Activities of Daily Life*

Advisor: *Prof. Graziano Pravadelli*

October 2013 – March 2016

Master Degree Computer Science and Engineering at University of Verona

Degree: *LM-32 - Master Degree Computer Science and Engineering*

Title of the thesis: *Automatic generation of self-adaptive TLM protocols from PSL assertions*

Supervisor: *Prof. Graziano Pravadelli*

Assistant Supervisor: *Dr. Francesco Stefanni*

Description: *Automatic generation of self-adaptive TLM protocols starting from PSL assertions. These assertions capture the communication protocol of an RTL target design. From these statements a self-adaptive intermediate module is automatically implemented that does not need to be regenerated if the initiator module involved in the communication is modified unlike the common transactors that are strictly dependent from initiator and target modules.*

Graduation date: *17/03/2016*

Grade: *110/110 cum laude*

October 2010 – March 2014

Bachelor Degree in Computer Science at University of Verona

Degree: *L-31 - Bachelor Degree in Computer Science*

Title of the thesis: *Graphical User Interface for TSL (TestBench Specification Language) Generators*

	<p><u>Supervisor:</u> <i>Prof. Graziano Pravadelli</i></p> <p><u>Description:</u> <i>Implementation of an human-machine interface for TSL (TestBench Specification Language). The application was written in C# using .NET framework all implemented with Visual Studio.</i></p> <p><u>Graduation date:</u> <i>19/03/2014</i></p> <p><u>Grade:</u> <i>95/110</i></p>
September 2005 – June 2010	<p>Maturity at the State Industrial Technical Institute of Higher Education Carlo Anti</p> <p><u>Title of the thesis:</u> <i>Programming a PLC aimed at managing the traffic lights system of a road intersection</i></p> <p><u>Graduation date:</u> <i>28/06/2010</i></p> <p><u>Grade:</u> <i>84/100</i></p>
Work experiences	
October 2016 – June 2018	<p>Teaching assistant</p> <p>Managing the laboratory activities and projects</p> <p><u>Course Name:</u> <i>Operating Systems</i></p> <p><u>Coordinator:</u> <i>Prof. Graziano Pravadelli</i></p> <p><u>Disciplinary sector:</u> <i>ING-INF/05 - Information Processing Systems</i></p> <p><u>Language of instruction:</u> <i>Italian</i></p> <p>Department of Computer Science, University of Verona, Verona (IT)</p>
April 2016 – September 2016	<p>Scholarship Holder</p> <p><u>Supervisor:</u> <i>Dr. Matteo Cristani</i></p> <p><u>Activity:</u> <i>Design of an architecture based on IoT devices (e.g., Single Board Computer (SBC), Single Board Micro-controller (SBM)) aiming to facilitate the controllability and observability of industrial plants from a remote web-app application. Specifically, we defined an approach to implement the control strategy of industrial plants through Extended Finite State Machines (EFSMs).</i></p> <p>Department of Computer Science, University of Verona, Verona (IT)</p>
Occasionally	<p>Salesman during electronic fairs, at modelism stand.</p>
July 2010 – October 2013	<p>Apprentice Pontatore at Pontek, Srl</p> <p>Via Capezzagna Storta, 16/1 46048 ROVERBELLA - Mantova (ITALIA) - Tel +39 0376 693693</p>
October 2012 – March 2013	<p>200 hours internship at University of Verona.</p> <p><u>Description:</u> <i>Implementation of an human-machine interface for an testbench generator based on constraints. During this internship I was placed in a work environment and in touch with IT professionals. The application on which I worked, was written in C# using .NET framework, all implemented with Visual Studio.</i></p>
July 2009 – October 2009	<p>Ordinary worker, warehouseman. at Pontek, Srl</p> <p>Via Capezzagna Storta, 16/1 46048 ROVERBELLA - Mantova (ITALIA) - Tel +39 0376 693693</p>
May 2009 – June 2009	<p>160 hours intership at Macrì Electrical Systems of Macrì Raffaele</p> <p>VIA CARLO POMA, 27 F, VILLAFRANCA DI VERONA, VERONA - Tel. +39 045 637 0913</p>
July 2008 – September 2008	<p>Agricultural, temporary worker at Azienda Agricola Saccardi</p> <p>Via Piave. 175 - 46048 Malavicina - Mantova (Italia)- Tel.+39 0376 696052</p>
March 2008	<p>160 hours intership at Ogheri Electricians/Vinicio</p>

Via Piave, 177 - 46048 Roverbella - Mantova (Italia) - Tel.+39 0376 696830

July 2007 – September 2007

Agricultural, temporary worker at Azienda Agricola Saccardi
Via Piave. 175 - 46048 Malavicina - Mantova (Italia)- Tel.+39 0376 696052

July 2007 – September 2007

Agricultural, temporary worker at Azienda Agricola Biologica Pietropoli
Strada Quistello, 23 - 46048 Roverbella - Mantova (Italia) - Tel. +39 0376 696127

July 2006 – September 2006

Agricultural, temporary worker at Azienda Agricola Biologica Pietropoli
Strada Quistello, 23 - 46048 Roverbella - Mantova (Italia) - Tel. +39 0376 696127

Publications

Authors Demrozi, Florenc, Graziano Pravadelli, and Francesco Stefanni
Title Automatic generation of self-adaptive transactors from PSL assertions.
Conference Specification and Design Languages (FDL), 2016 Forum on. IEEE, 2016.

Authors Demrozi, Florenc, Riccardo Zucchelli, and Graziano Pravadelli
Title Exploiting sub-graph isomorphism and probabilistic neural networks for the detection of hardware Trojans at RTL.
Conference 2017 IEEE International High Level Design Validation and Test Workshop (HLDVT). IEEE, 2017.

Personal skills and competences

Technical Skills

- Operating Systems: Linux (Debian/Ubuntu/RedHat) Distributions, Windows
- Tools: Microsoft Office, Libre Office, Visual Studio
- Programming and Markup Languages: C, C++, C#, MATLAB, Java, HDL languages, Bash, \LaTeX , PHP, SQL, HTML, XHTML, CSS, Javascript

Mother tongue(s)

Albanian, Italian

Other Languages

English

*Self-assessment
European level^(*)*

Understanding		Speaking		Writing
Listening	Reading	Spoken interaction	Spoken production	
B2 Independent user	B2 Independent user	B2 Independent user	B2 Independent user	B2 Independent user

^(*) Common European Framework of Reference (CEF) level

English

Driver License

B

Informations

Personal Skills

- Accurate, Adaptable, Ambitious, Attentive
- Confident, Cooperative, Determined
- Energetic, Enterprising, Hardworking
- Honest, Independent, Motivated
- Organized, Responsible, Versatile

Personal interests

Music, Chess, Sport(Soccer), Swimming, History, Geography